


BAR CODE LABEL  		<h2 style="margin: 0;">U.S. PATENT APPLICATION</h2>			
SERIAL NUMBER  07/743,383		FILING DATE  08/21/91	CLASS  156	GROUP ART UNIT  1304	
APPLICANT	JUN-ICHI KONNO, MIE, JAPAN; KEISUKE SHINAGAWA, KANAGAWA, JAPAN; TOSHIYUKI ISHIDA, KANAGAWA, JAPAN; TAKAHIRO ITO, KANAGAWA, JAPAN; TESTSUO KONDO, KANAGAWA, JAPAN; FUKASHI HARADA, MIE, JAPAN; SHUZO FUJIMURA, TOKYO, JAPAN.  <div style="margin-top: 20px;">           **CONTINUING DATA*****            VERIFIED             _____         </div> <div style="margin-top: 40px;">           **FOREIGN/PCT APPLICATIONS*****            VERIFIED                      PCT                      PCT/JP91/00861                      06/26/91                                                 JAPAN                      2-171791                      06/27/90             _____         </div>				
STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS	FILING FEE RECEIVED	ATTORNEY DOCKET NO.
JPX	7	22	4	\$ 560.00	911333
ADDRESS	ARMSTRONG, NIKAIDO, MARMELESTEIN, KUBOVCIK & MURRAY 1725 K ST. N.W., STE. 1000 WASHINGTON, DC 20006				
TITLE	METHOD FOR PRODUCING SEMICONDUCTOR INTEGRATED CIRCUITS AND APPARATUS USED IN SUCH METHOD				
<p>This is to certify that annexed hereto is a true copy from the records of the United States Patent and Trademark Office of the application as filed which is identified above.</p> <p>By authority of the COMMISSIONER OF PATENTS AND TRADEMARKS</p> <div style="display: flex; justify-content: space-between; margin-top: 10px;"> <span>Date</span> <span>Certifying Officer</span> </div>					